

FIG. 1

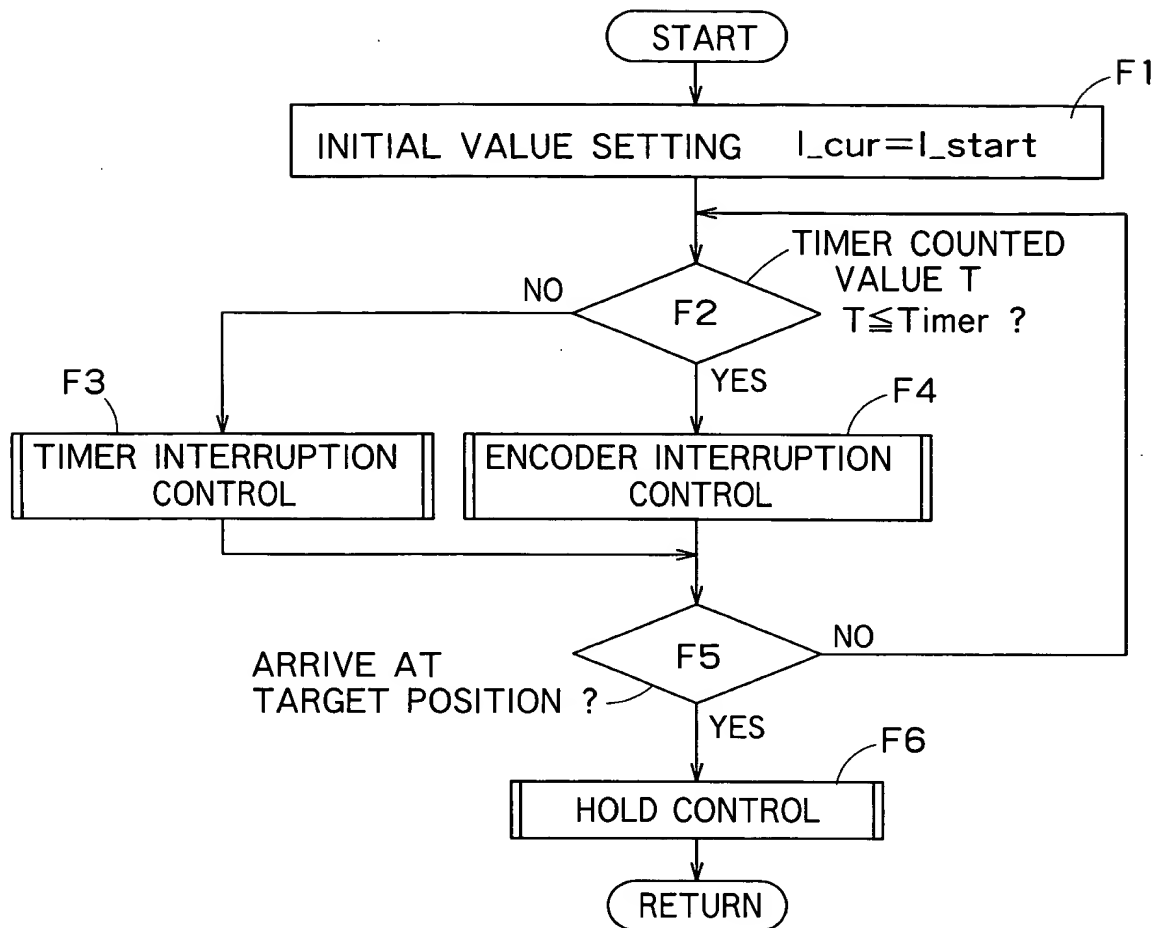


FIG. 2

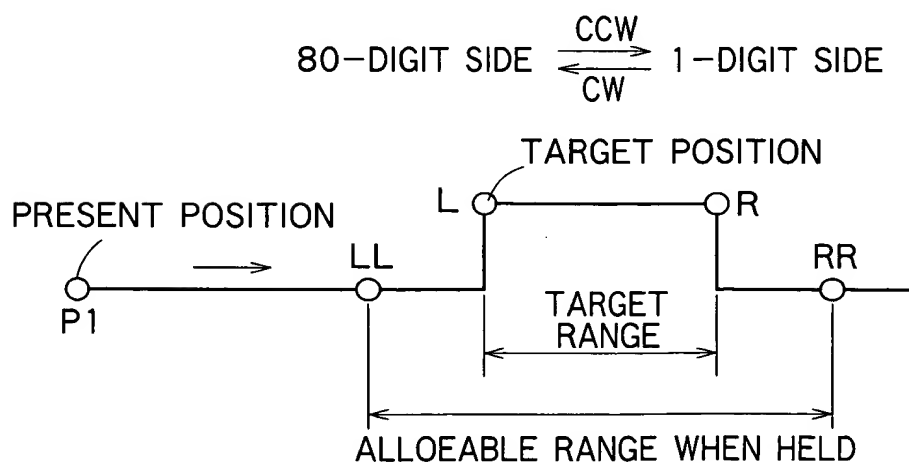


FIG. 3

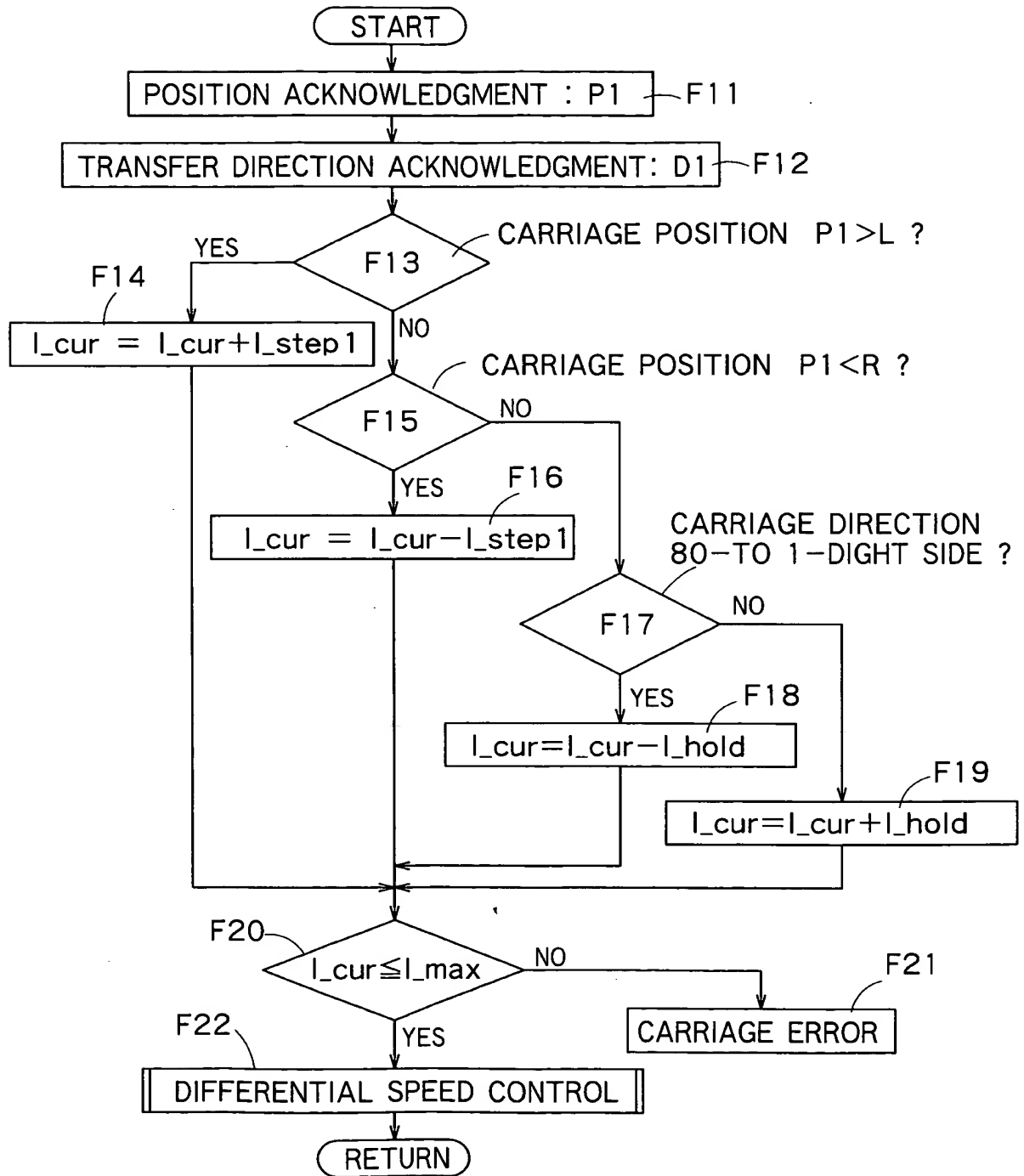


FIG. 4

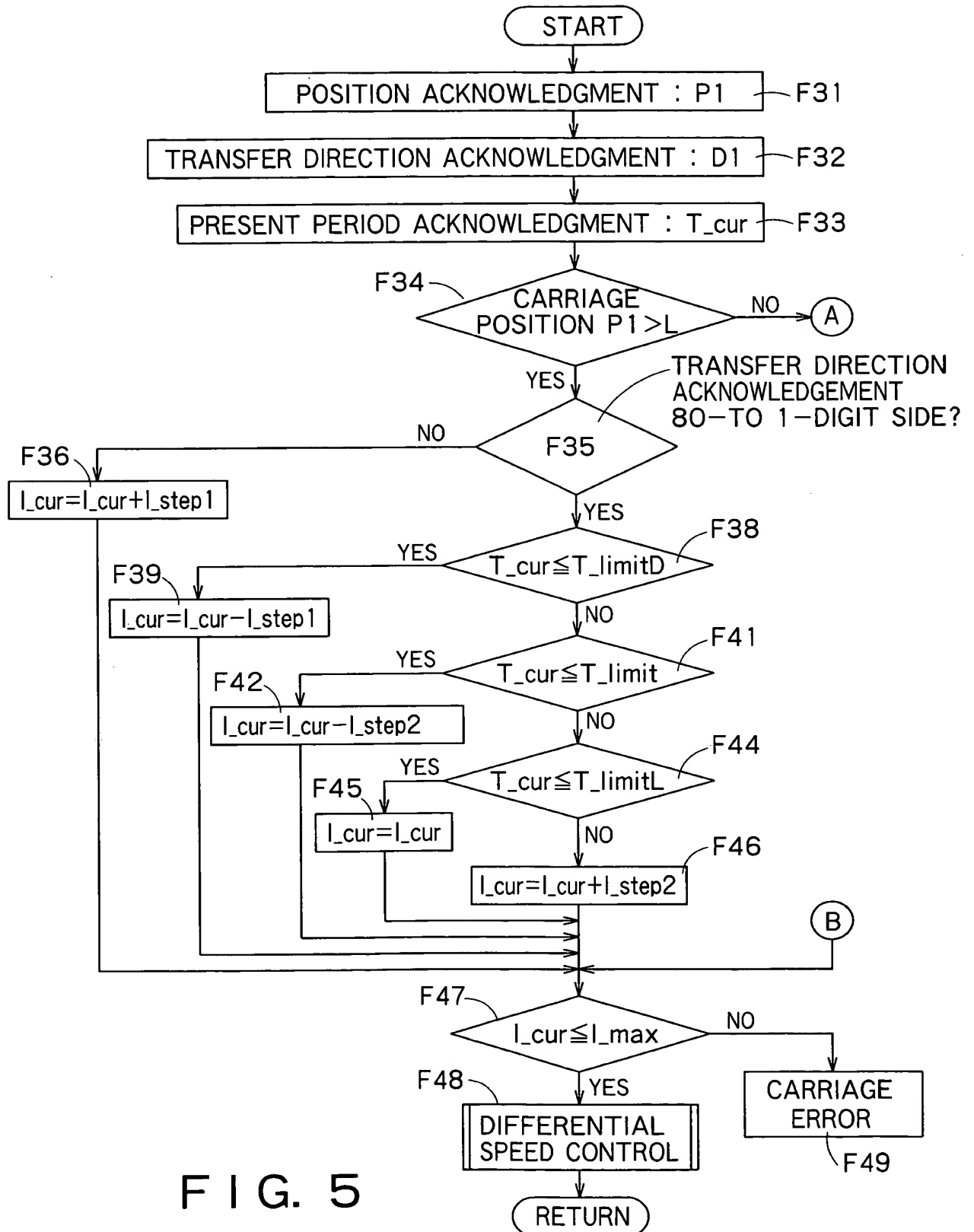


FIG. 5

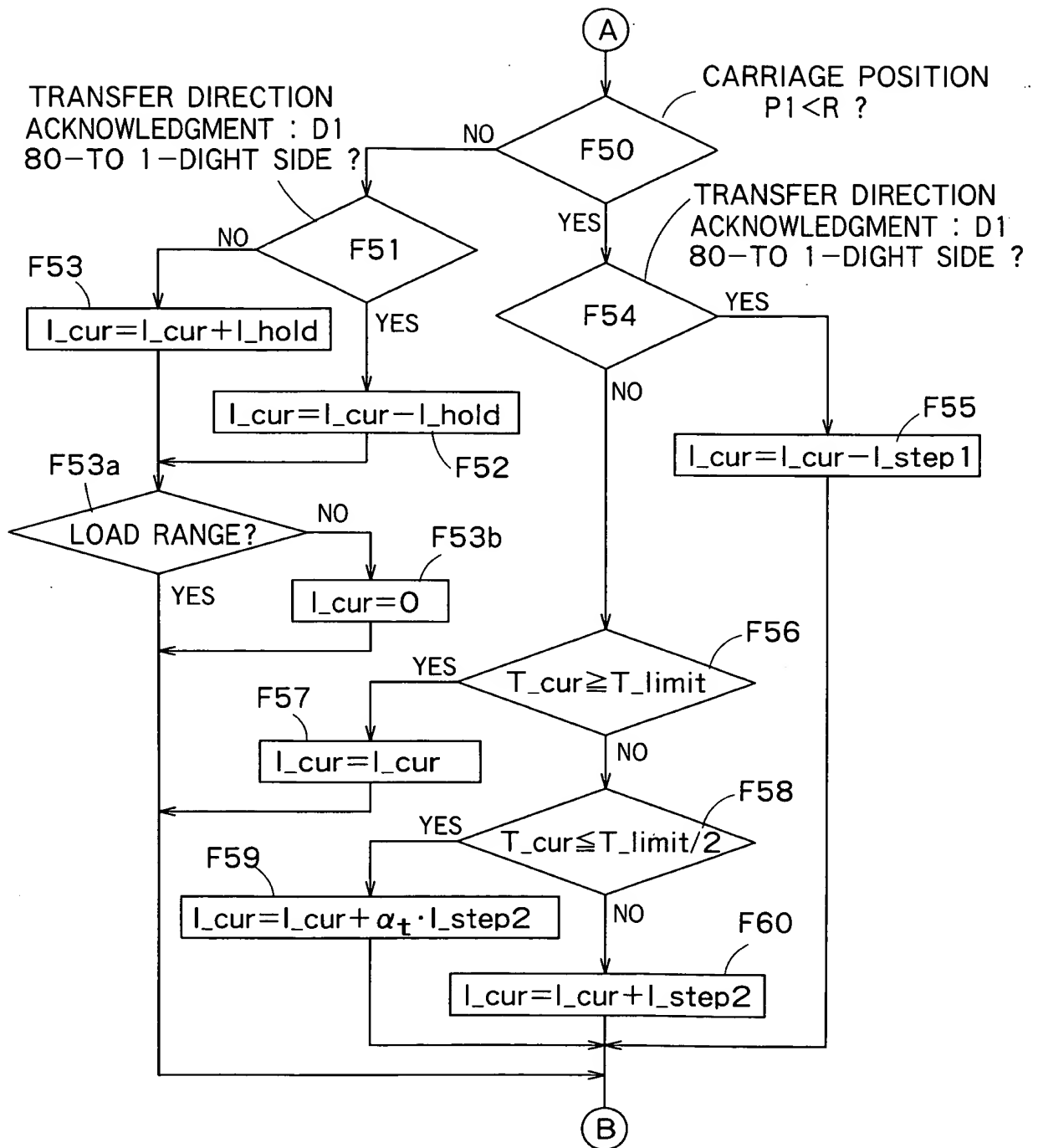


FIG. 6

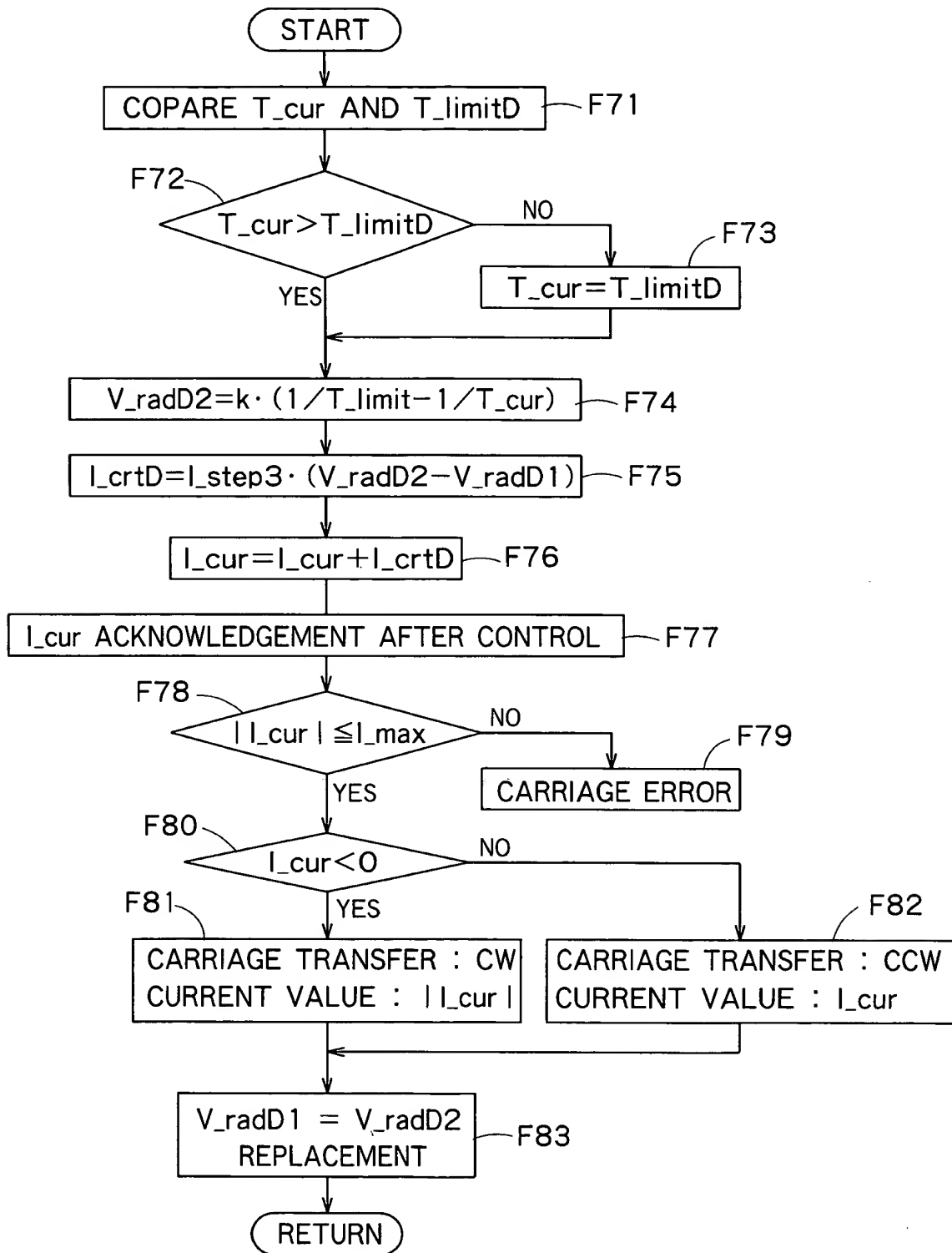


FIG. 7

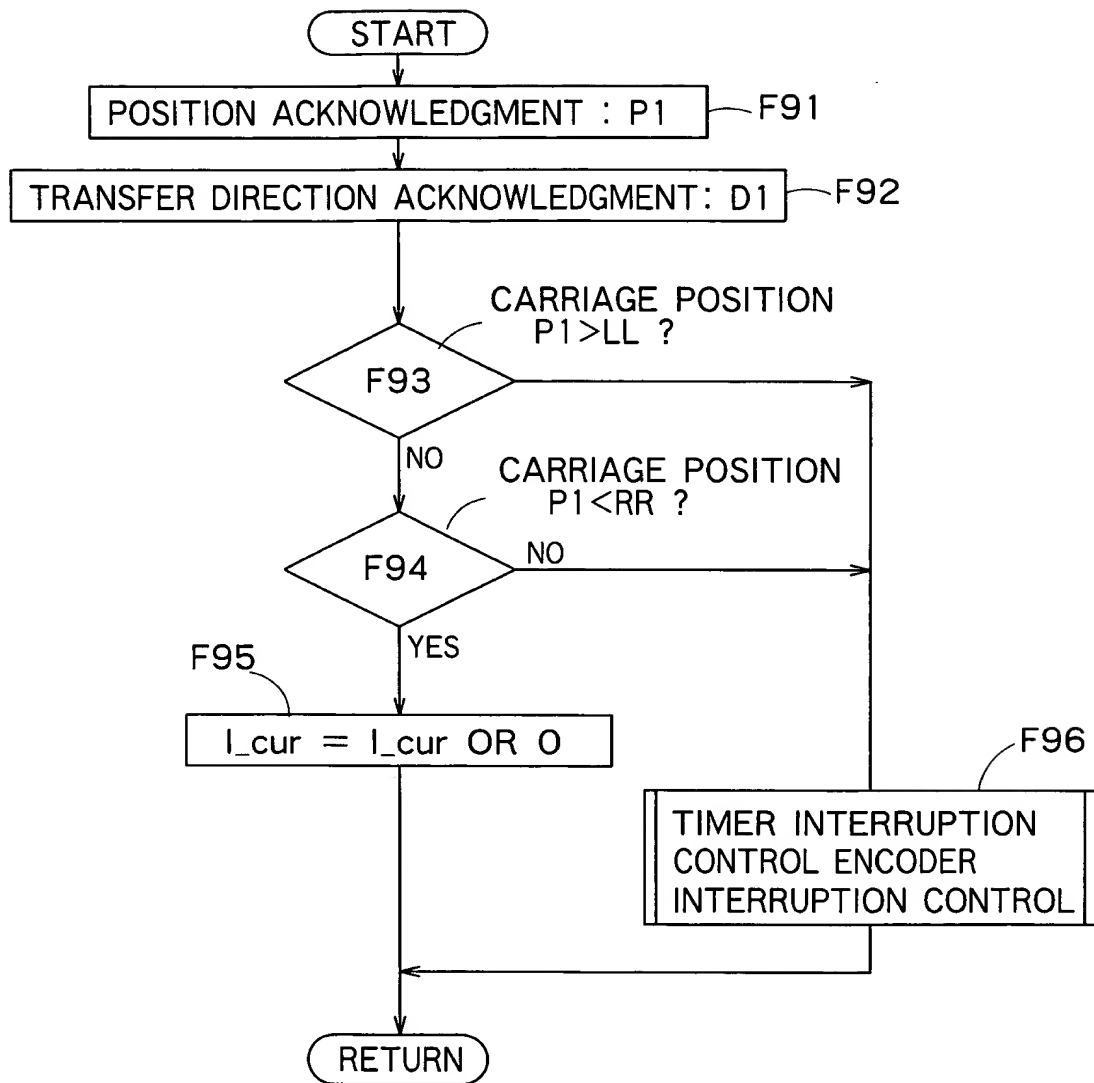


FIG. 8

SPEED	TARGET SPEED [cps]	OPERATION	TIMER [μs]	T_limitL [μs]	T_limit [μs]	T_limitD [μs]	I_step1 [mA]	I_step2 [mA]	I_step3 [mA]	I_hold [mA]	I_start [mA]	I_max [mA]
CREEP- ING	a v1	LOGICAL SEEK	b Tm1	b TL1	b T1	b TD1	c 11	c 21	c 31	c f1	c s1	c m1
NORMAL	a v2	RUBBING	b Tm2	b TL2	b T2	b TD2	c 12	c 22	c 32	c f2	c s2	c m2
HIGH	a v3	WIPING	b Tm3	b TL3	b T3	b TD3	c 13	c 23	c 33	c f3	c s3	c m3

FIG. 9

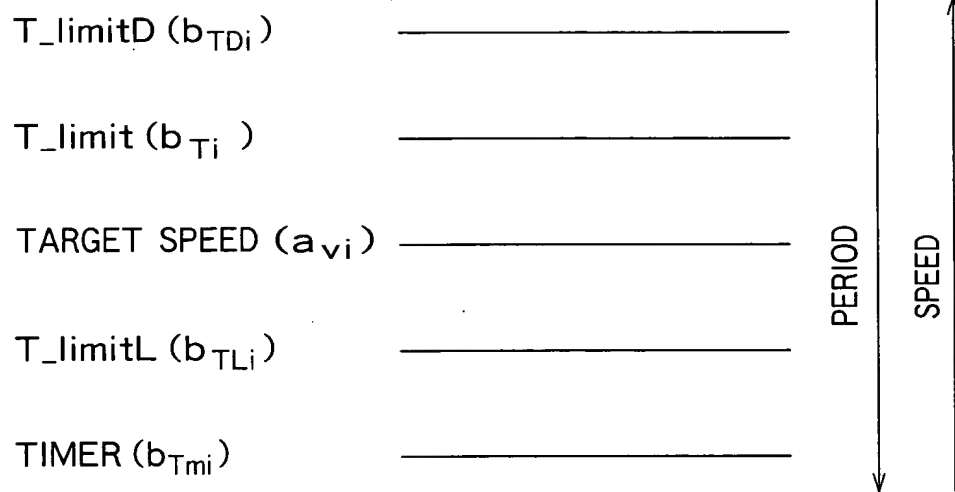


FIG. 10

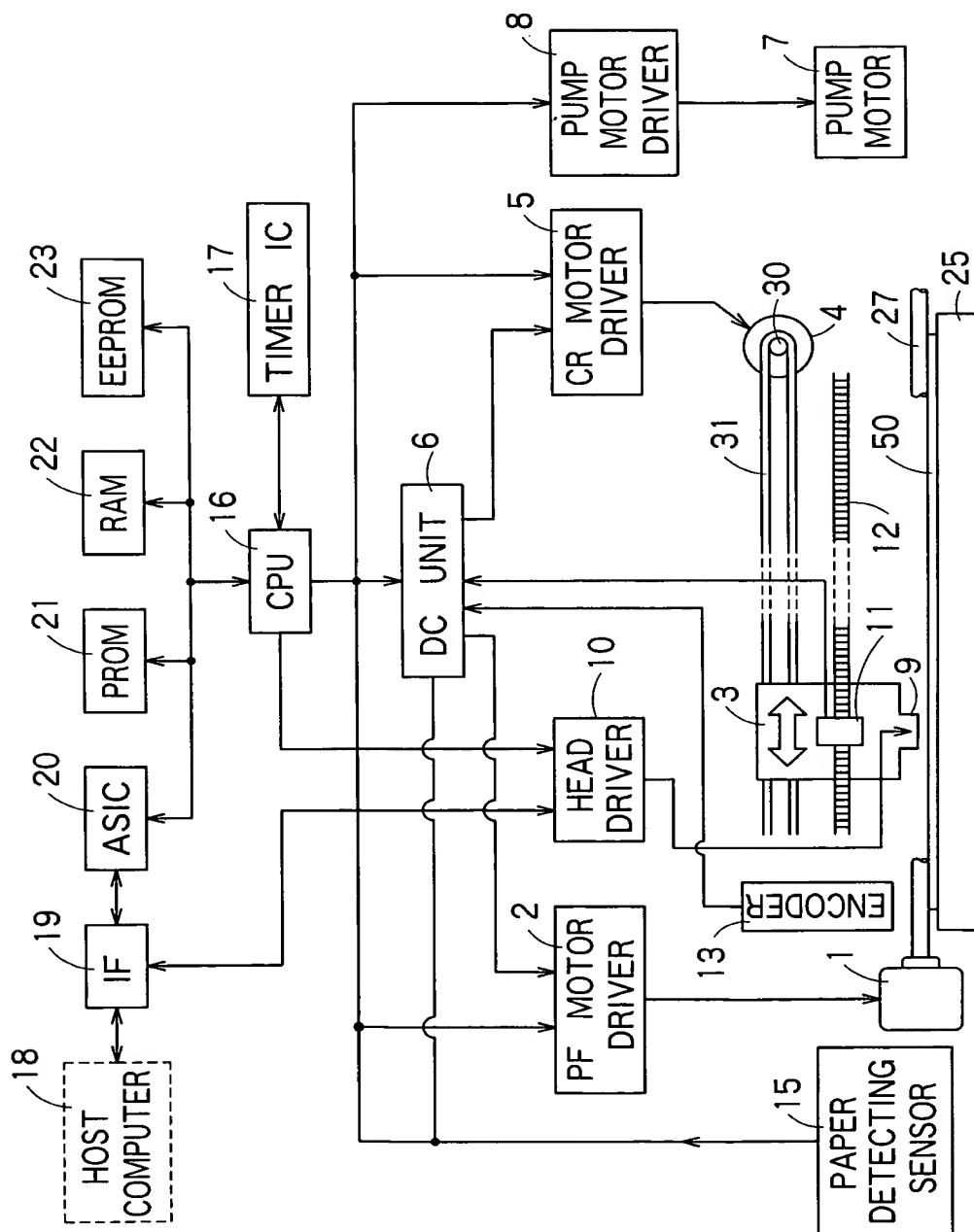


FIG. 11

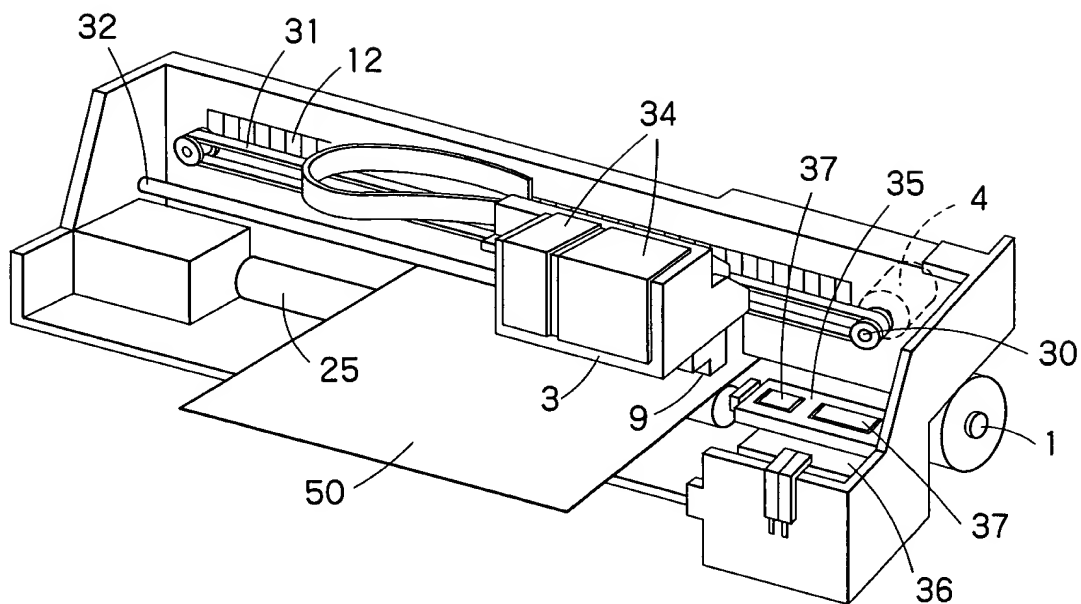


FIG. 12

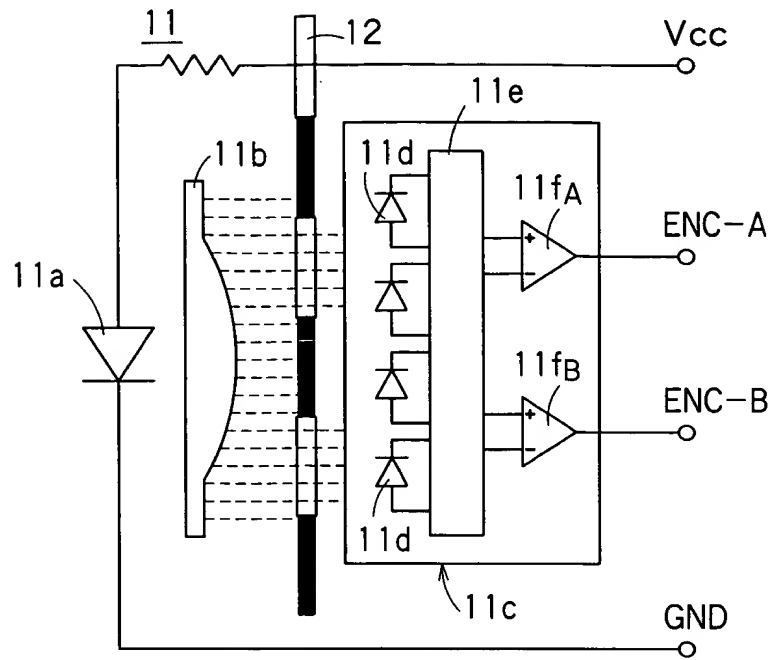


FIG. 13

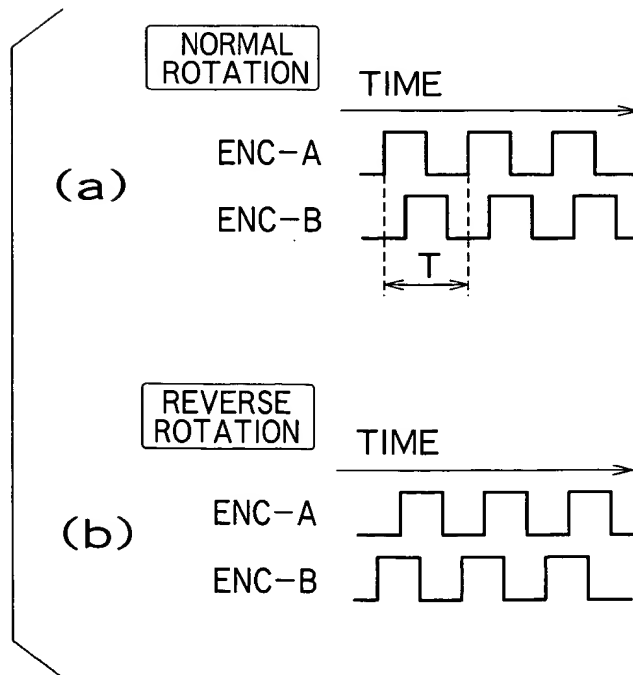


FIG. 14

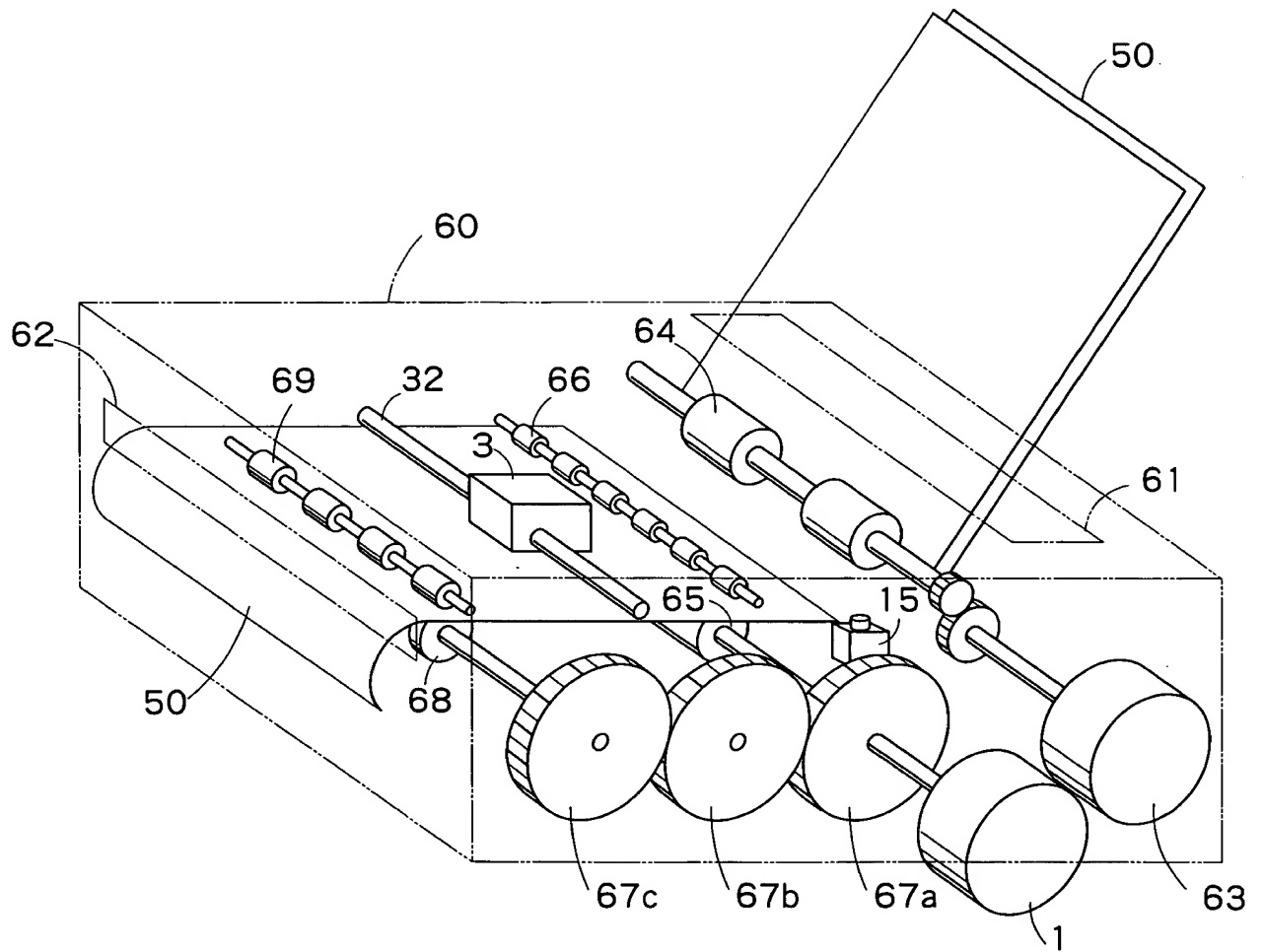


FIG. 15

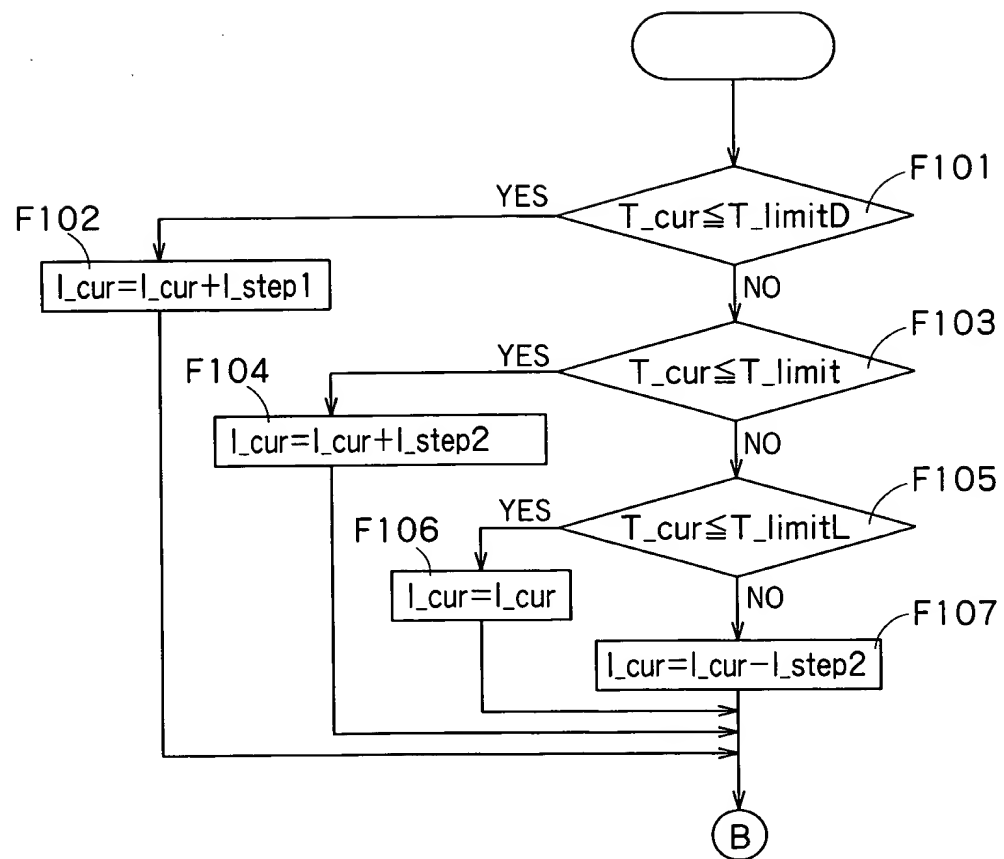


FIG. 16

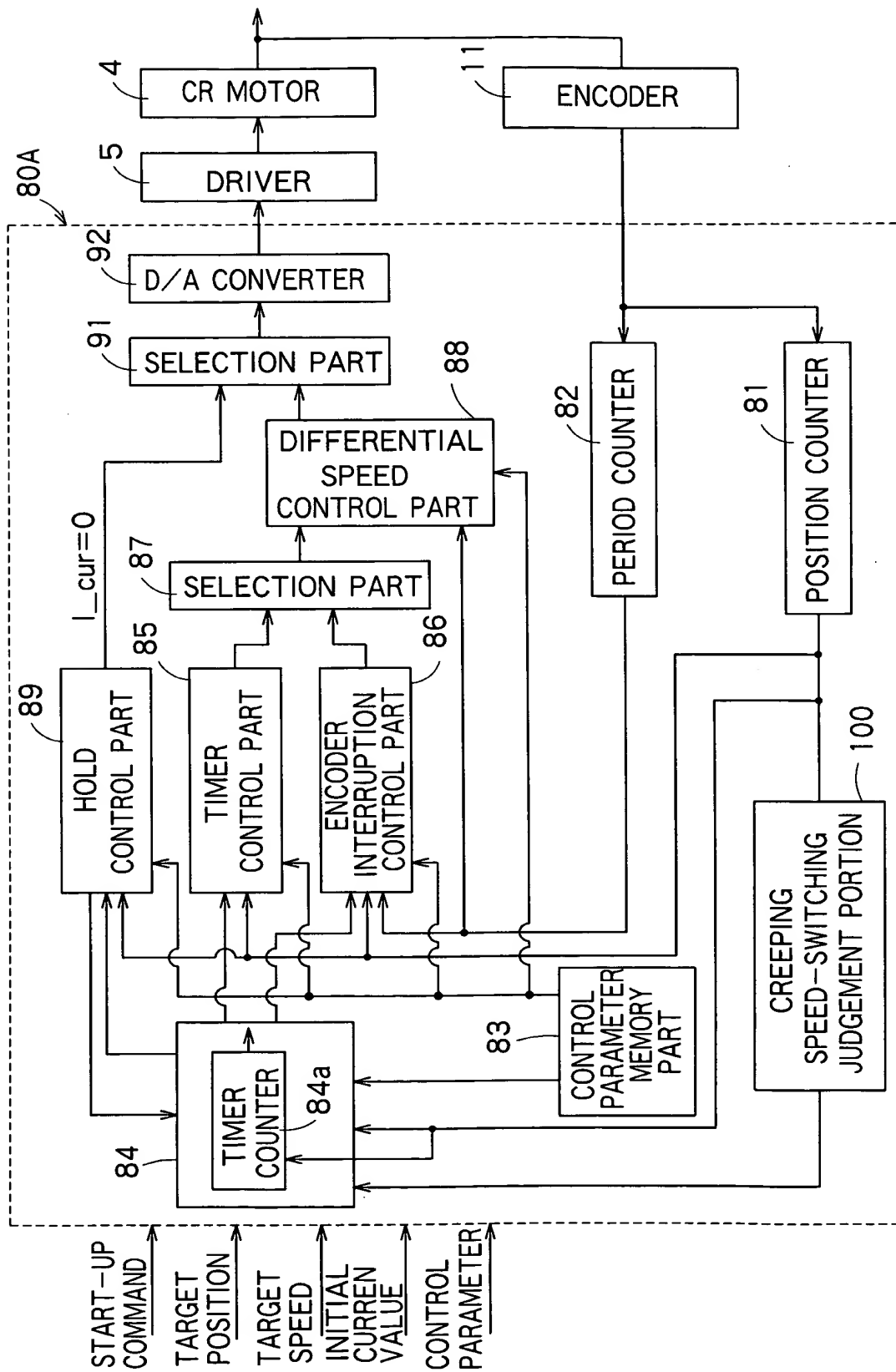


FIG. 17

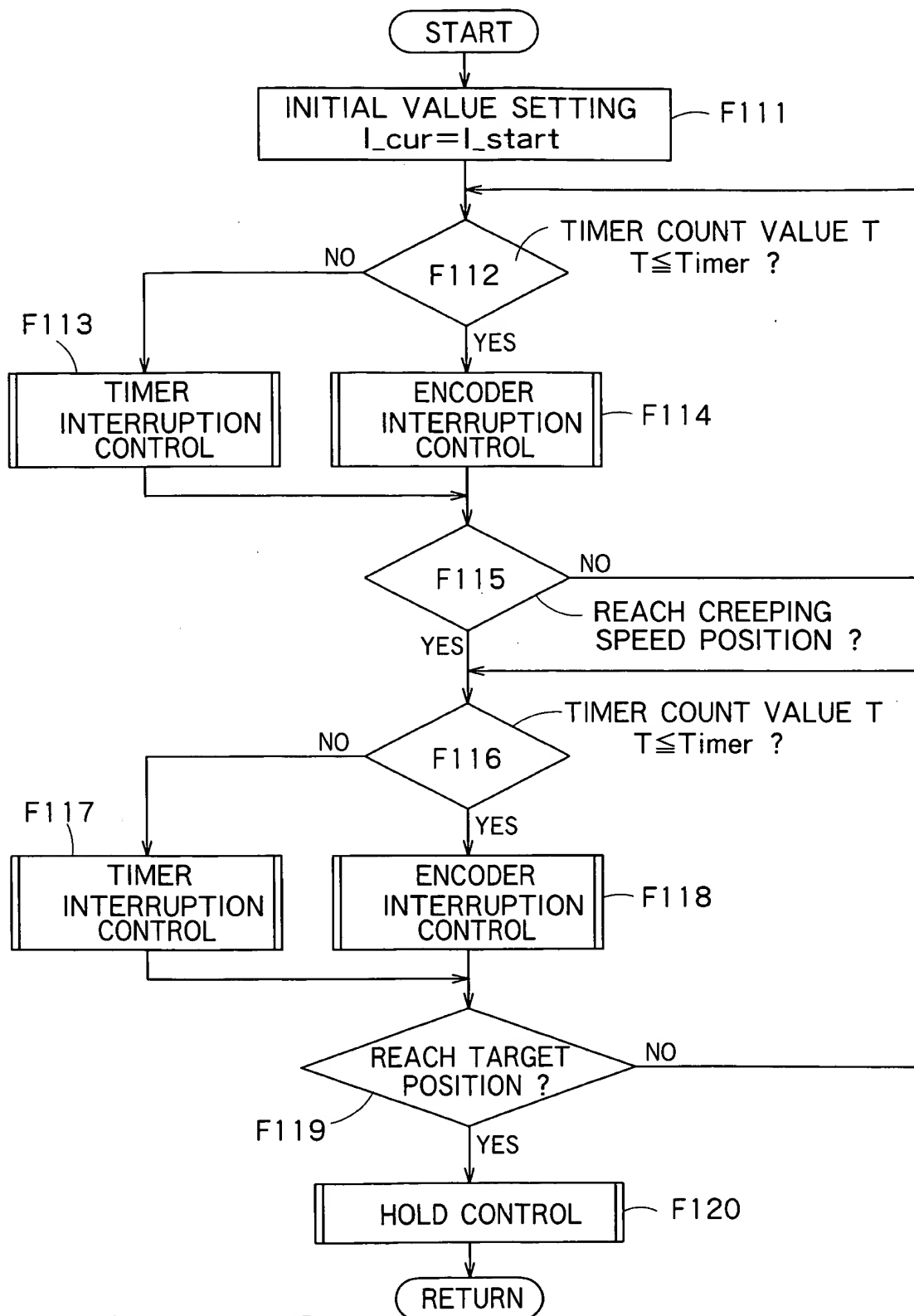


FIG. 18

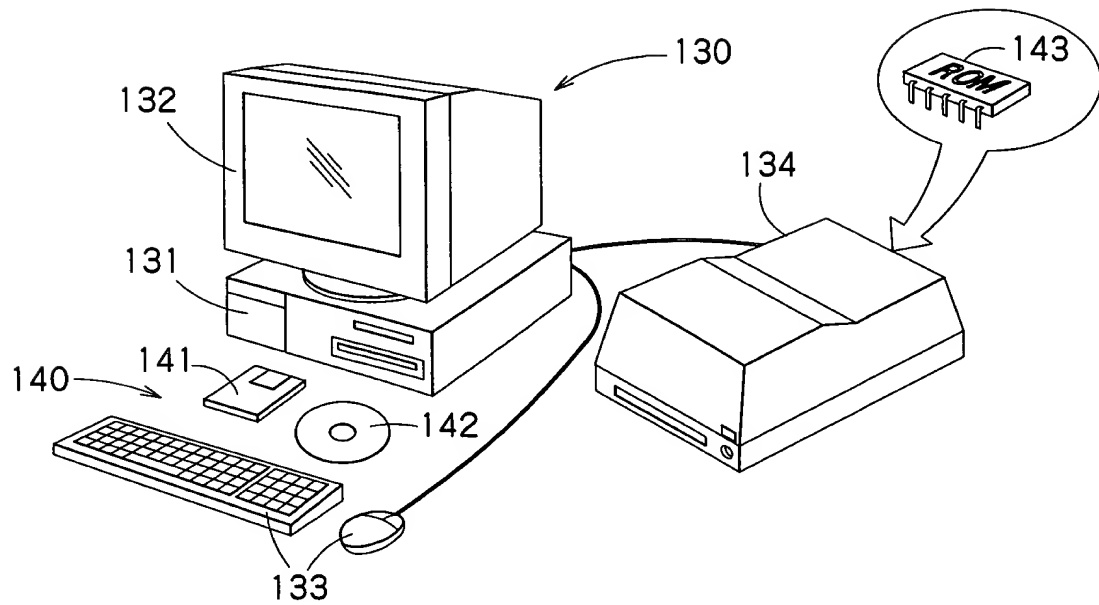


FIG. 19

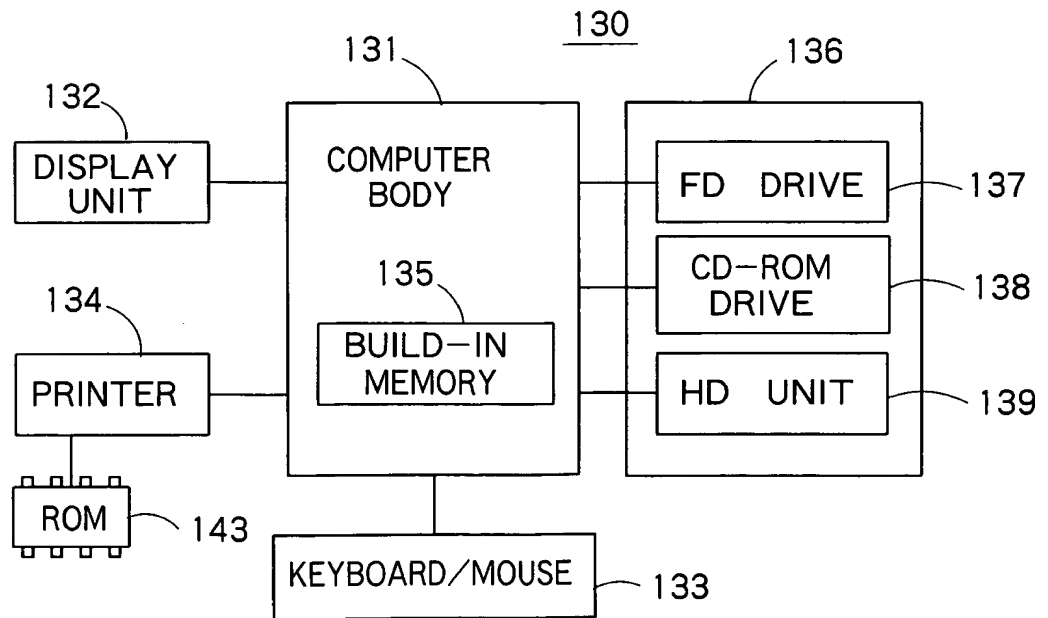


FIG. 20

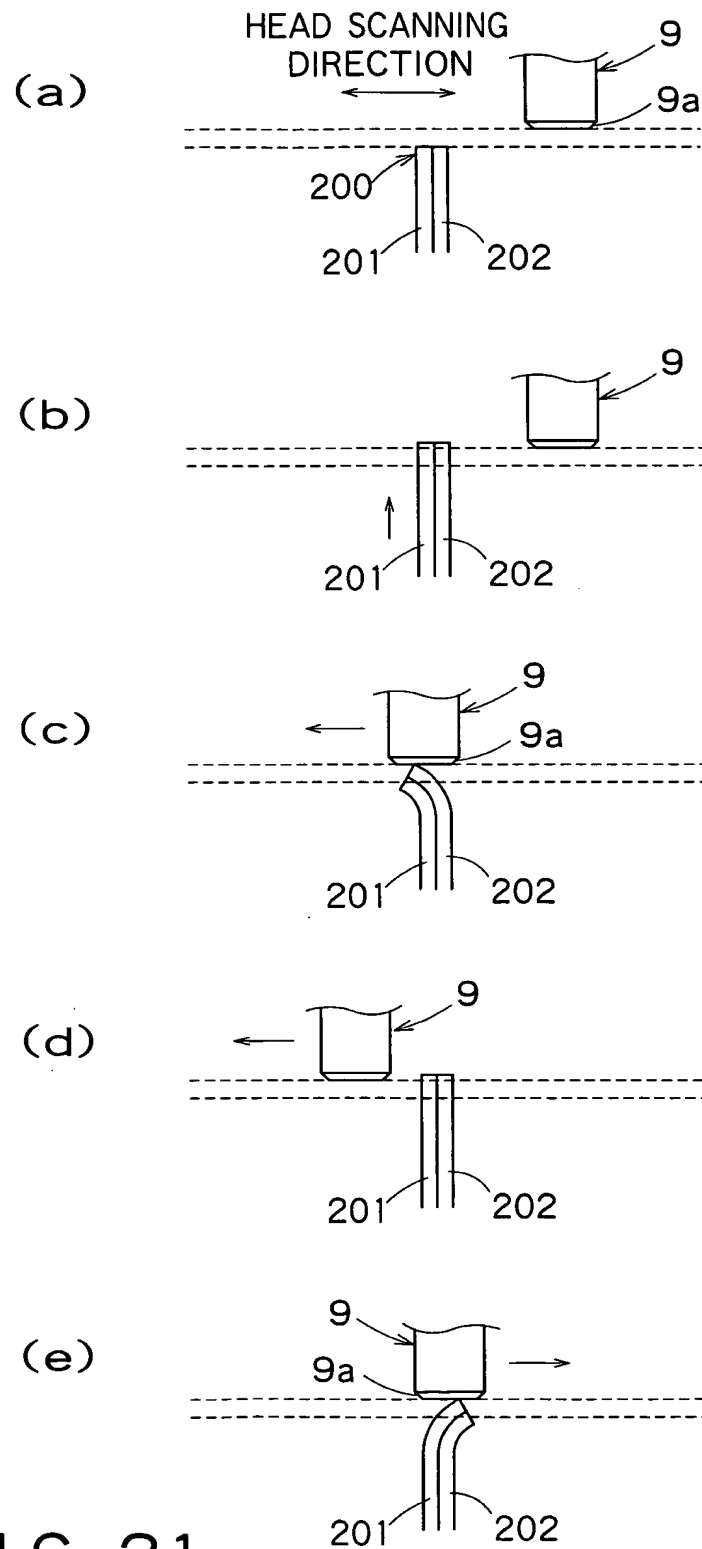


FIG. 21